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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

| Sheet | 1 | of | 2 |

Complete if Known	
Application Number	(not yet assigned)
Filing Date	1-26-2001
First Named Inventor	BANERJEE, et al.
Group Art Unit	(not yet assigned)
Examiner Name	(not yet assigned)
Attorney Docket Number	NWU-P001

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[5] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

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Substitute for form 1449B/PTO **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** *(use as many sheets as necessary)*	**Complete if Known**	
	Application Number	(not yet assigned)
	Filing Date	1-26-2001
	First Named Inventor	BANERJEE, et al.
	Group Art Unit	(not yet assigned)
	Examiner Name	(not yet assigned)
Sheet **2** of **2**	Attorney Docket Number	NWU-P001

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
C.C.	1.	P.BANERJEE, N.SHENOY, A.CHOUDHARY, S.HAUCK, C.BACHMANN, M.HALDAR, P.JOISHA, A.JONES, A.KANHARE, A.NAYAK, S.PERIYACHERI, M.WALKDEN, D.ZARETSKY; A MATLAB Compiler For Distributed, Heterogeneous, Reconfigurable Computing Systems, Proc. IEEE Symposium on Field-Programmable Custom Computing Machines, FCCM'00, April 2000, 10 pages, Electrical and Computer Engineering; Northwestern University, Evanston, IL-60208 - USA	
C.C.	2.	MALAY HALDAR, ANSHUMAN NAYAK, ALOK CHOUDHARY and PRITH BANERJEE: AUTOMATED SYNTHESIS OF PIPELINED DESIGNS ON FPGAS FOR SIGNAL AND IMAGE PROCESSING APPLICATIONS DESCRIBED IN MATLAB; Proc. of ASIA PACIFIC DESIGN AUTOMATION CONFERENCE (ASP-DAC) January, 2001; 4 pages; MACH Design Systems, Inc. Schaumberg, IL USA	
C.C.	3.	MALAY HALDAR, ANSHUMAN NAYAK, ALOK CHOUDHARY and PRITH BANERJEE: FPGA HARDWARE SYNTHESIS FROM MATLAB; VLSI DESIGN CONFERENCE, BANGALORE, INDIA; January 2001, 8 pages, Center For Parallel and Distributed Computing, Northwestern University, Evanston, IL 60208-3118 USA	

Examiner Signature	*[signature]*	Date Considered	1/28/03

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